

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
	10982165-2		
	APPLICANT		
	James E. McCormick, Jr., et al.		
	FILING DATE	GROUP	
	10/21/2003		

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
/DJH/	1A	5,859,999	01/12/1999	Morris et al.	
/DJH/	1B	5,860,017	01/12/1999	Sharangpani et al.	
/DJH/	1C	5,922,065	07/13/1999	Hull et al.	
/DJH/	1D	6,237,077	05/2001	Sharangpani et al.	
/DJH/	1E	4,794,517	12/1998	Jones et al.	
/DJH/	1F	4,935,867	06/1990	Wang et al.	
/DJH/	1G	5,515,518	05/1996	Stiles et al.	
/DJH/	1H	5,822,575	10/1998	Tran, Thang M.	
/DJH/	1I	5,590,358	12/1996	Mizrahi-Shalom et al	
/DJH/	1J	6,253,305	06/2001	Matsuzaki et al.	
/DJH/	1K	4,530,050	07/1985	Fukunaga et al.	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

/DJH/	1Q	"Patents shed light on Merced" by Alexander Wolfe, Electronic Engineering Times, February 15, 1999, pages 43 and 44
/DJH/	1R	"Trading Conflict and Capacity Aliasing in Conditional Branch Predictors" by Michaud, et al., Proceedings of the 24th Annual International Symposium on Computer Architecture, 1997, 9 pages
/DJH/	1S	"Alternative Implementations of Two-Level Adaptive Branch Prediction", by Tse-Yu Yeh, et al., Department of Electrical Engineering and Computer Science, University of Michigan, July 1992, pages 124-134

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/DJH/	2A	5,761,470	06/1998	Yoshida, Toyohiko	
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	2E				
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	2H				
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	2O					
	2P					

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

/DJH/	2Q	Internet Publication "The Alpha 21264 Microprocessor: Out-of-Order Execution at 600 Mhz" by R. E. Kessler, August 1998, pages 1-30
/DJH/	2R	"Instrumentation and Optimization of Win32/Intel Executables Using Etch", by Romer et al., University of Washington, 8 pages
/DJH/	2S	"Intel's 1A-64 Application Developer's Architecture Guide" (Rev 1.0, Order Number 245188-001, May 1999)

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	3A				
	3B				
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	3L					
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	3N					
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

/DJH/	3Q	"Fine-Grained Dynamic Instrumentation of Commodity Operating System Kernels" by Tamches, et al., Computer Sciences Department, University of Wisconsin (7 pages 2-sided)
/DJH/	3R	"The Paradyn Parallel Performance Measurement Tools" by Miller, et al., Computer sciences Department of University of Wisconsin, pages 1-23
/DJH/	3S	Mississippi College, CSC 110 - Computer Mathematics, December 1998, http://www.mc.edu/courses/csc/110/module3_2.html

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